



- Drafts**
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- (345) data adj2 strobe adj2 pulse
 - (20762) writ\$5 near5 (activ\$8)
 - (9) (generat\$5 near5 (data adj2 strobe adj2 pulse)) and (writ\$5 near5 (activ\$8))
 - (18) (data adj2 strobe adj2 pulse) and (writ\$5 near5 (activ\$8))
 - (53) generat\$5 near5 (data adj2 strobe adj2 pulse)
 - (19479) writ\$5 adj2 command
 - (2929) data adj2 strobe adj2 (pulse or signal)
 - (108) (writ\$5 adj2 command) same (data adj2 strobe adj2 (pulse or signal))
 - (105) ((writ\$5 adj2 command) same (data adj2 strobe adj2 (pulse or signal))) not (generat\$5 near...
 - (828) 365/193
 - (4849) 365/189.05
 - (5073) 365/233
 - (2929) data adj2 strobe adj2 (pulse or signal)
 - (915) writ\$5 same (data adj2 strobe adj2 (pulse or signal))
 - (53) (writ\$5 same (data adj2 strobe adj2 (pulse or signal))) and 365/193
 - (304) writ\$5 same (data adj2 strobe adj2 (pulse or signal)) same timing
 - (20) (writ\$5 same (data adj2 strobe adj2 (pulse or signal)) same timing) and 365/193
 - (33) ((writ\$5 same (data adj2 strobe adj2 (pulse or signal))) and 365/193) not ((writ\$5 same (d...
 - (25) (writ\$5 same (data adj2 strobe adj2 (pulse or signal)) same timing) and 365/189.05
 - (75) (writ\$5 same (data adj2 strobe adj2 (pulse or signal)) same timing) and 365/233
 - (34) (MORZANO near2 CHRISTOPHER).in.
 - (393) (U near2 WEN).in.
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DBs: USPAT, US-PGPUB, EPO, JPO, DERWENT, IBW ☒ Plurals

Default operator: OR

☒ Highlight all hit terms initially

BRS term SAR term Image Text HTML

	U	1	2	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Cl	Inventor	S	C	P
1	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6034916 A	20000307	13	Data masking circuits and methods for integrated circuit me	365/233	365/193		Lee, Jae-hyeong	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
2	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6229757 B1	20010508	56	Semiconductor memory device capable of securing large latch mar	365/233	365/189.05; 365/193;		Nagata, Kyoichi et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
3	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6407963 B1	20020618	19	Semiconductor memory device of DDR configuration having improve	365/233.5	365/233		Sonoda, Takahiro et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
4	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6529993 B1	20030304	20	Data and data strobe circuits and operating protocol for double data	711/105	365/189.01; 365/193		Rogers, Jim L. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
5	<input type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6728162 B2	20040427	20	Data input circuit and method for synchronous semiconductor mem	365/233	365/189.05; 365/219		Lee, Jung-bae et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>

Hls Details HTML

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